

FIG. 1

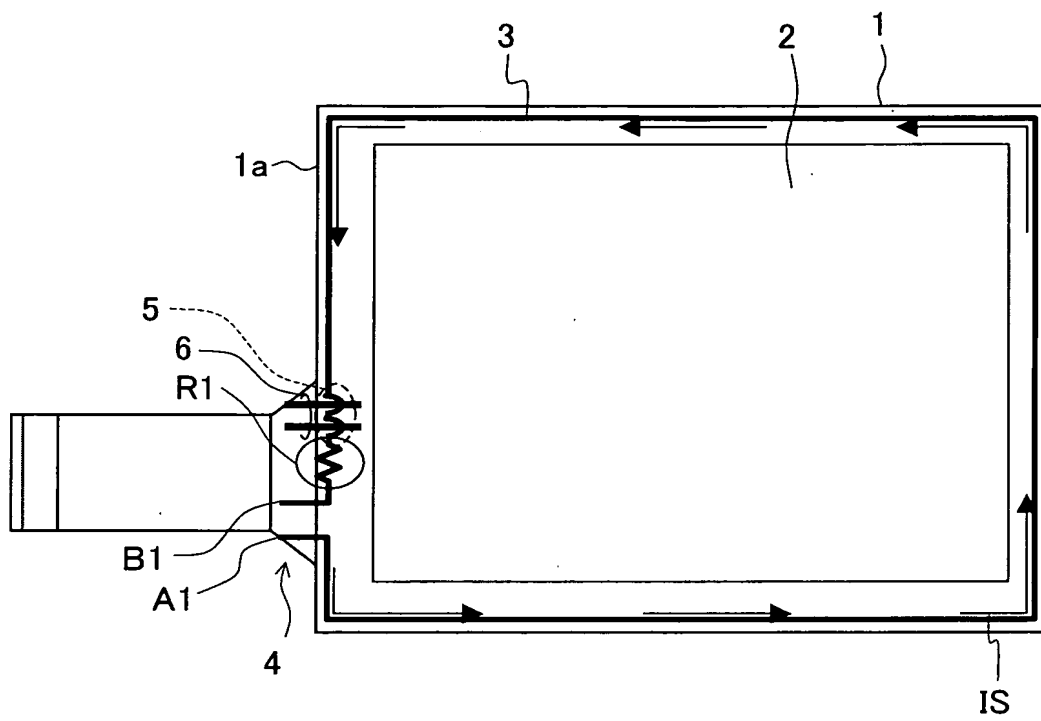


FIG. 2A PRINCIPAL PORTION

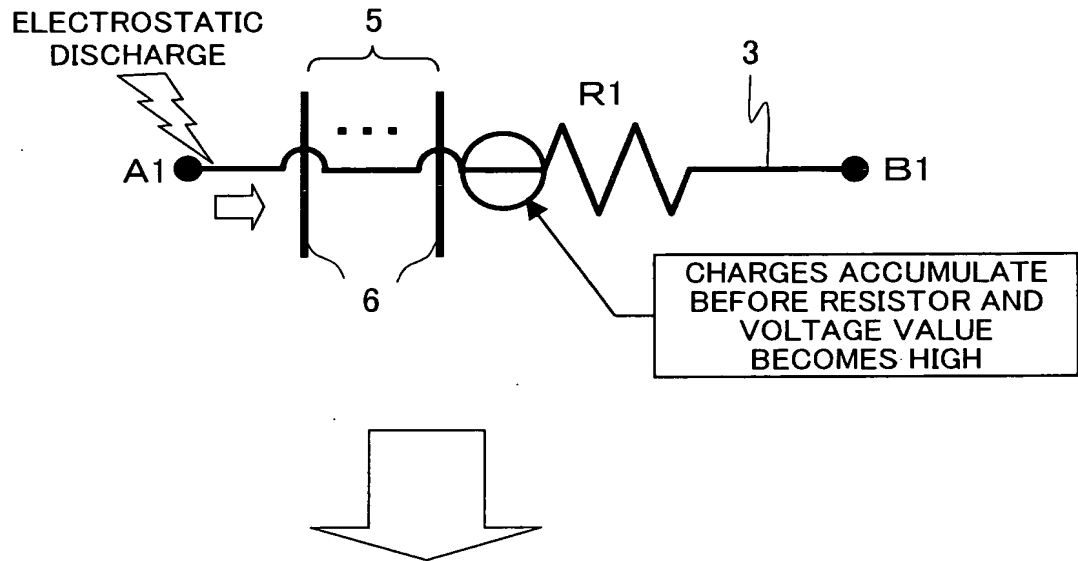


FIG. 2B EQUIVALENT CIRCUIT

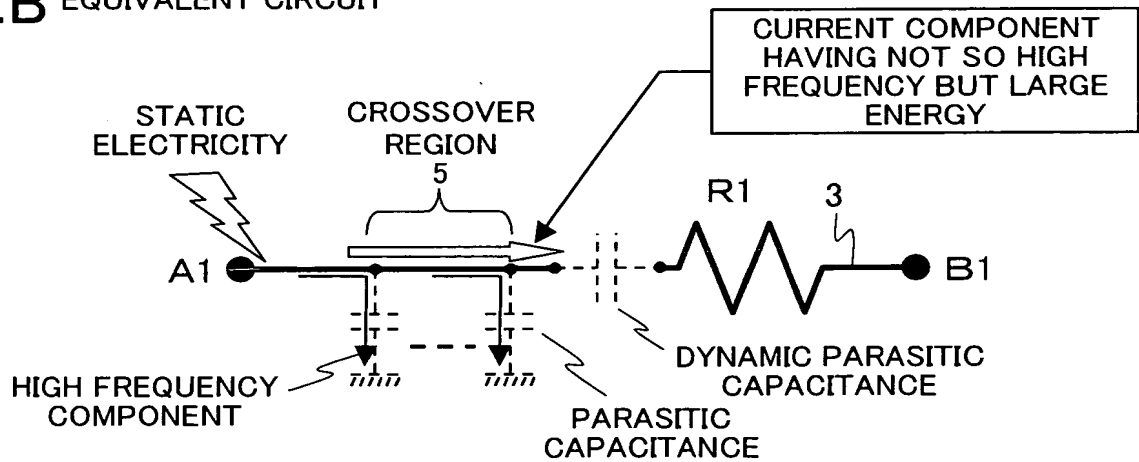


FIG. 3

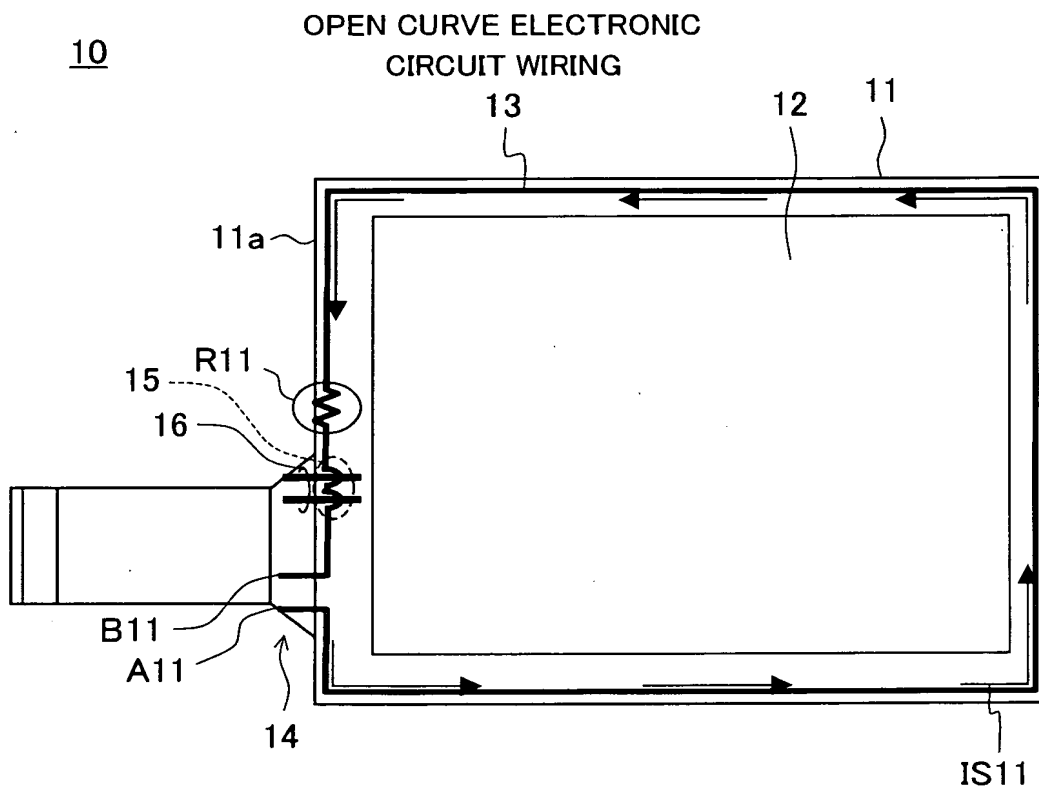


FIG. 4

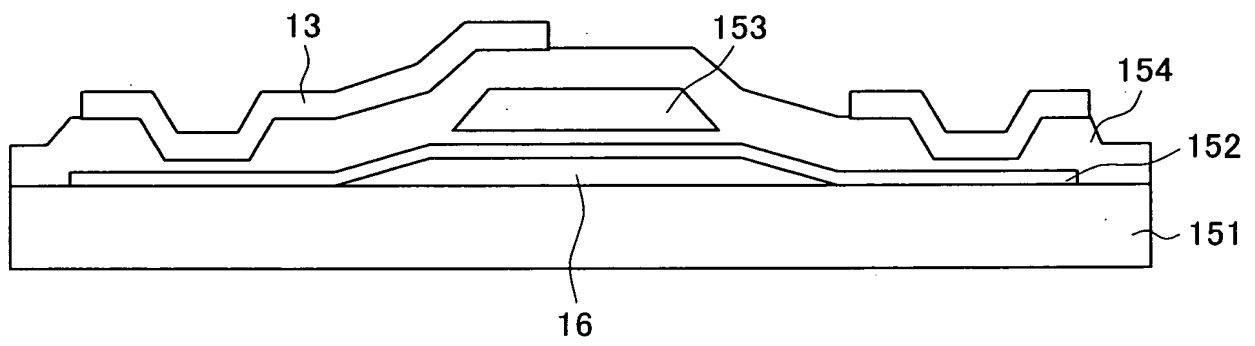


FIG. 5A PRINCIPAL PORTION

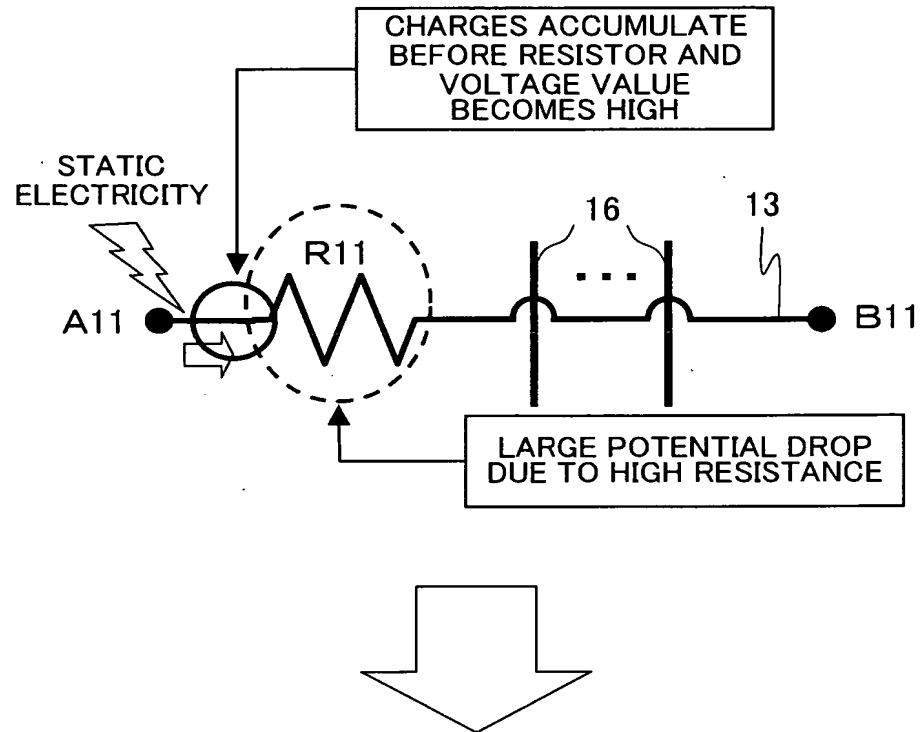


FIG. 5B EQUIVALENT CIRCUIT

